

IP for int 0	00000h
CS for int 0	

IP for int 0ffh	003ffh
CS for int 0ffh	

GDT segment

--

DISP segment in RM

DISPVGA proc far
-----
retf

DATA1 segment

--

TASK1 segment

--

ini\_dat segment in RM

gdt_desc (B=51000h, L=03ffh)
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ini\_cod segment in RM

```

start:
PLACE 5100h,GDT segment
PLACE 5200h,DISP segment
PLACE 5400h,DATA1 segment
PLACE 5500h,TASK1 segment
mov ax, 5000h
mov ds, ax
mov si, 4000h
mov word ptr [si], OFFSET return
push cs
pop ax
mov word ptr [si+2], ax
cli
mov ax,ini_dat
mov ds,ax
lgdt gdt_desc
smsw ax
or ax, 1
lmsw ax
jmp priv1
priv1:
pjmp 0028h,0000h

return:
mov ax, 5400h
mov ds, ax
mov si, offset text4
call DISPVGA
mov ah, 4ch
int 21h

```

GDT segment in PM

null_desc (sel=00h)	51000h
vga_desc (sel=08h)	
disp_desc (sel=10h)	
stack1_des (sel=18h)	
data1_desc (sel=20h)	
task1_desc (sel=28h)	

DISP segment in PM

DISPVGA proc far	52000h
-----	
retf	

STACK1 segment in PM

	53000h
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DATA1 segment in PM

off_ret dw ?	54000h
seg_ret dw ?	
line dw 0	
clear db "	
text1 db "PRIVILE	
text2 db "Task1 st	
text3 db "Task1 fin	
text4 db "REAL M	

TASK1 segment in PM

```

start5:
mov ax,18h
mov ss,ax
mov sp,1000h
mov ax,08h
mov es,ax
mov ax,20h
mov ds,ax
mov cx,25
again51:
mov si,offset clear
pcall 10h,0h
loop again51
mov line,320
-----
-----
switch_to_real_mode:
mov eax, cr0
and eax, 7fffffeh
mov cr0, eax
jmp real
real:
mov ax, 5000h
mov ss, ax
mov sp, 4000h
retf

```