

IDT segment	int_gate (csel=48h)	50000h
in PM	int_gate (csel=48h)	int1
	int_gate (csel=48h)	int2
	int_gate (csel=48h)	int3
	int_gate (csel=48h)	int4
	int_gate (csel=48h)	int5
	int_gate (csel=48h)	int6
timer	int_gate (csel=48h)	int7
keyboard	task_gate (csel=58h)	int8
	int_gate (csel=38h)	int9
	int_gate (csel=48h)	int10
	int_gate (csel=48h)	int11
	int_gate (csel=48h)	int12
net_card	int_gate (csel=48h)	int13
	int_gate (csel=48h)	int14
	int_gate (csel=48h)	int15

GDT segment	null_desc (sel=00h)	51000h
in PM	reset_desc (sel=08h)	
	vga_desc (sel=10h)	
	disp_desc (sel=18h)	
	stack1_desc (sel=20h)	
	data1_desc (sel=28h)	
	task1_desc (sel=30h)	
	keyb_desc (sel=38h)	
	task2_desc (sel=40h)	
	idle_desc (sel=48h)	
	tss1_desc (sel=50h)	
	tss2_desc (sel=58h)	
	stack2_desc (sel=60h)	

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start:
PLACE 5100h,GDT segment
PLACE 5200h,DISP segment
PLACE 5400h,DATA1 segment
PLACE 5500h,TASK1 segment
-----
-----
mov ax, 5000h
mov ds, ax
ini_cod segment in RM mov si, 4000h
mov word ptr [si], OFFSET return
push cs
pop ax
mov word ptr [si+2], ax
cli
mov ax,ini_dat
mov ds,ax
lgdt gdt_desc
lidt idt_desc
smsw ax
or ax, 1
lmsw ax
jmp priv1
priv1:
pjmp 0030h,0000h

return:
mov ax, 5400h
mov ds, ax
mov si, offset text4
call DISPVGA
mov ah, 4ch
int 21h

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TASK1 segment in PM start5: 55000h
cli
mov ax, 50h
ltr ax
-----
-----
again5:
task1 activity flag
-----
test ex_flag, 1
jz again5
exit:
switch_into_RM

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KEYB segment in PM in al, 60h 56000h
cmp al,1 ; ESC ?
je exit6
display char
rfi:
mov al,20h
out 20h,al
pop si
pop ax ; EOI
iret
exit6:
mov ex_flag,1
jmp rfi

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TASK2 segment in PM start7: 57000h
display time
task2 activity flag
-----
mov al,20h ; EOI
out 20h,al
iret
jmp start7

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